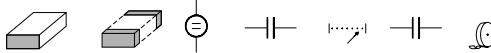




# Multilayer Chip Capacitors

## Ordering code system



B37940- **K** **5** **010-** **D** **5** **60**

### Packaging

- 62 = blister tape, reel dia. 180 mm
- 72 = blister tape, reel dia. 330 mm
- 60 = cardboard tape, reel dia. 180 mm
- 70 = cardboard tape, reel dia. 330 mm
- 01 = bulk case

**Decimal place** for cap. values < 10 pF, otherwise not used

### Capacitance tolerance

(tolerance code in acc. with IEC 62, standard values bold)

C0G / NP0 / CH	X7R / B char. and X8R	Z5U (Y5U) / F char.
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$C_R < 10 \text{ pF}$ : **B** =  $\pm 0,1 \text{ pF}$   
**C** =  $\pm 0,25 \text{ pF}$   
**D** =  $0,5 \text{ pF}$

$C_R \geq 10 \text{ pF}$ : **F** =  $\pm 1 \%$   
**G** =  $\pm 2 \%$   
**J** =  $\pm 5 \%$   
**K** =  $10 \%$   
**M** =  $\pm 20 \%$   
**N** =  $\pm 50 \%$

**Capacitance, coded**    010 = 1 pF    101 = 100 pF    103 = 10 nF    105 = 1  $\mu\text{F}$   
100 = 10 pF    102 = 1 nF    104 = 100 nF    474 = 470 nF

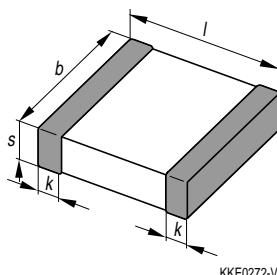
<b>Rated voltage</b>	Rated voltage [Vdc]	16	25	50	100	200	500
	Code	9	0	5	1	2	3

**Terminations** Standard: K = silver/nickel/tin  
J = silver palladium    for chip sizes 0402, 0603, 0805, 1206, 1210  
for chip sizes 1812, 2220  
for conductive adhesion: all sizes

## Type and size

Chip size (inch / mm)	Temperature characteristics C0G / NP0 / CH	X7R / B char.	X8R	Z5U (Y5U) / F char.
<b>0402 / 1005</b>	B37920	B37921		B37922
<b>0603 / 1608</b>	B37930	B37931		B37932
<b>0805 / 2012</b>	B37940	B37941	B37541	B37942
<b>1206 / 3216</b>	B37871	B37872	B37472	B37873
<b>1210 / 3225</b>	B37949	B37950	B37550	B37951
<b>1812 / 4532</b>		B37953		B37954
<b>2220 / 5750</b>		B37956		B37957

## Dimensions and construction

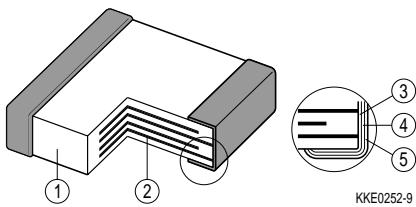


Size inch/mm	<i>l</i>	<i>b</i>	<i>s</i>	<i>k</i>
<b>0402/1005</b>	$1,0 \pm 0,10$	$0,50 \pm 0,05$	$0,5 \pm 0,05$	0,2
<b>0603/1608</b>	$1,6 \pm 0,15^*)$	$0,80 \pm 0,10$	$0,8 \pm 0,10$	0,3
<b>0805/2012</b>	$2,0 \pm 0,20$	$1,25 \pm 0,15$	1,3 max.	0,5
<b>1206/3216</b>	$3,2 \pm 0,20$	$1,60 \pm 0,15$	1,3 max.	0,5
<b>1210/3225</b>	$3,2 \pm 0,30$	$2,50 \pm 0,30$	1,7 max.	0,5
<b>1812/4532</b>	$4,5 \pm 0,30$	$3,20 \pm 0,30$	1,3 max.	0,5
<b>2220/5750</b>	$5,7 \pm 0,40$	$5,00 \pm 0,40$	1,3 max	0,5

*\*) For bulk cases  $1,6 \pm 0,1$*

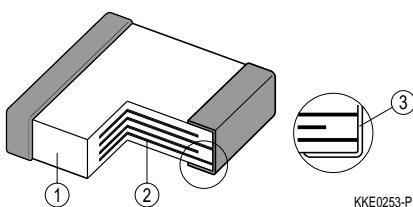
- Dimensions and tolerances in accordance with CECC 32101-801
- MLCC internal construction in accordance with EIA 469

Sizes 0402 through 1210 for soldering



No.	Name	Materials
1	Dielectric	Ceramics
2	Inner electrode	Pd or AgPd
3	Substrate electrode	Ag
4	Intermediate electrode	Ni
5	External electrode	Sn

Sizes 1812, 2220 for soldering  
All sizes for conductive adhesion



No.	Name	Materials
1	Dielectric	Ceramics
2	Inner electrode	Pd or AgPd
3	External electrode	AgPd

# Multilayer Chip Capacitors

## Electrical characteristics in brief

Temperature characteristic Standard	C0G/NP0/CH EIA	X7R/B char. EIA	X8R EIA	Z5U (Y5U)/ F char. <sup>1)</sup> EIA
Dielectric	Class 1	Class 2	Class 2	Class 2
Rated voltage $V_R$ <sup>2)</sup> Vdc	50/100/200	16/25/50/100/ 200/500	50	16/25/50
Climatic category (IEC 68-1)	55/125/56	55/125/56	55/150/56	30/085/56
Temperature range	– 55 ... + 125 °C	– 55 ... + 125 °C	– 55 ... + 150 °C	– 30 ... + 85 °C
Available capacitance values $C_R$ E series	1 pF ... 10 nF E12	220 pF ... 1 µF E12	1 nF ... 150 nF E12	1 nF ... 4,7 µF E6
Capacitance tolerance (standard in bold print)	$C_R < 10 \text{ pF}$ : $\pm 0,1 \text{ pF}$ <b><math>\pm 0,25 \text{ pF}</math></b> $\pm 0,5 \text{ pF}$  $C_R \geq 10 \text{ pF}$ : $\pm 1 \%$ <sup>2)</sup> $\pm 2 \%$ <sup>2)</sup> <b><math>\pm 5 \%</math></b> $\pm 10 \%$			<b><math>\pm 20 \%</math></b>
Temperature coefficient (tolerance)	$0 \pm 30 \cdot 10^{-6}/\text{K}$	–	–	–
Max. rel. capacitance change $\Delta C/C$ at $V_{\text{meas}}$	–	$\pm 15 \%$	$\pm 15 \%$	+22/– 56 %
Voltage test	$2,5 \cdot V_R/5 \text{ s}$			
Dissipation factor $\tan \delta$ (limit value)	$< 1,0 \cdot 10^{-3}$	$< 25 \cdot 10^{-3}$ $< 35 \cdot 10^{-3}$ (16 V)	$< 25 \cdot 10^{-3}$	$< 30 \cdot 10^{-3}$ $< 35 \cdot 10^{-3}$ (25 V) $< 70 \cdot 10^{-3}$ (16 V)
Insulation resistance <sup>4)</sup> at 25 °C 125 °C	$> 10^5 \text{ M}\Omega$ $> 10^4 \text{ M}\Omega$	$> 10^5 \text{ M}\Omega$ $> 10^4 \text{ M}\Omega$	$> 10^5 \text{ M}\Omega$ $> 10^4 \text{ M}\Omega$	$> 10^4 \text{ M}\Omega$ –
Time constant $\tau$ <sup>4)</sup> at 25 °C 125 °C	$> 1000 \text{ s}$ $> 100 \text{ s}$	$> 1000 \text{ s}$ $> 100 \text{ s}$	$> 1000 \text{ s}$ $> 100 \text{ s}$	$> 500 \text{ s}$ –

1) Y5U specification is also fulfilled.

2) 1 % and 2 % tolerance not for 200 V.

3) 5% tolerance not for 16 V, 200 V and 500 V.

4) For capacitance values exceeding 10 nF (C0G, X7R, X8R) and 47 nF (Z5U) the time constant  $\tau = C \cdot R_{\text{ins}}$  is given.